



DB-RM9000X2-S7

Schematic

Revision 2.0

2-Oct-02

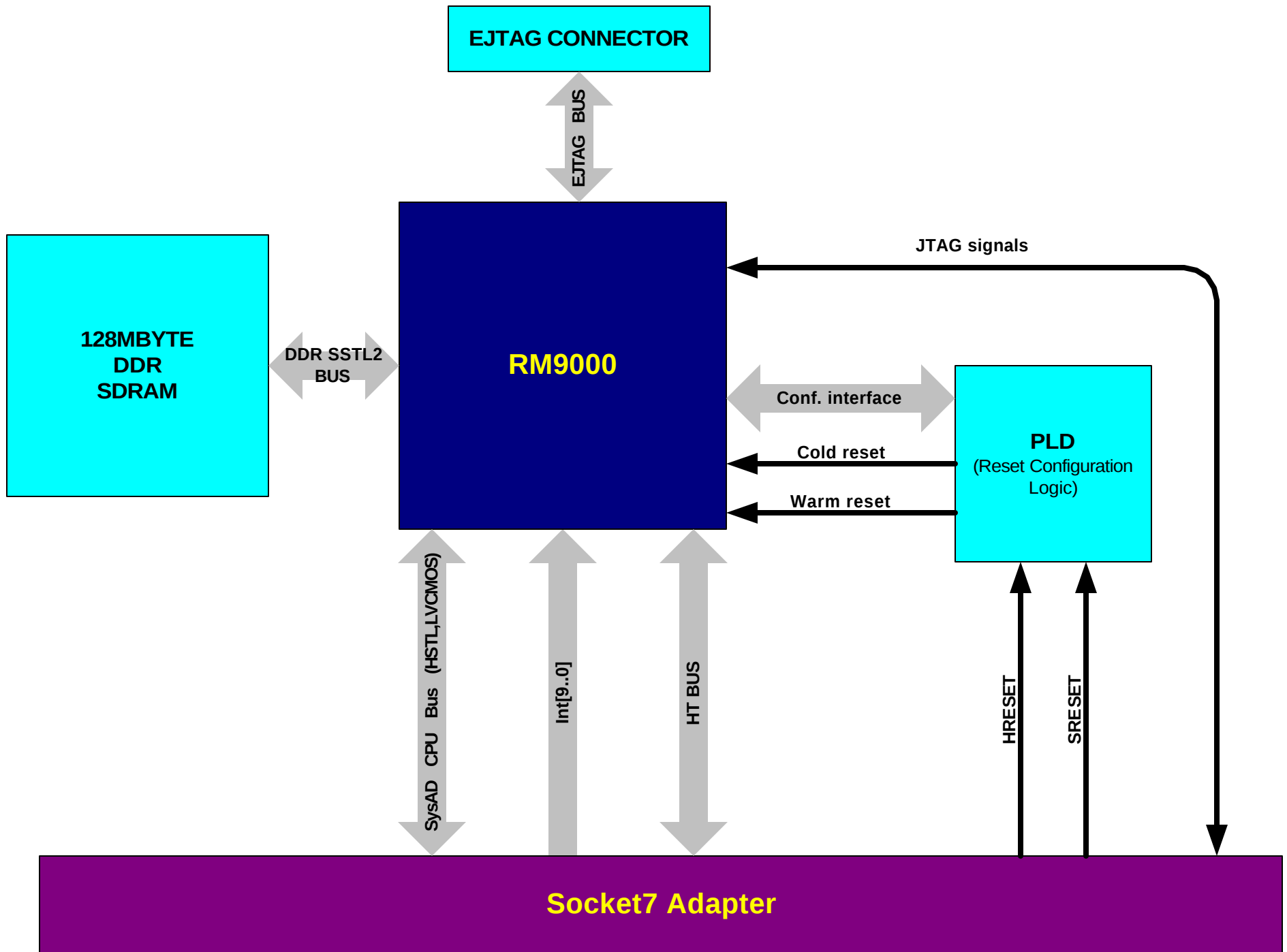
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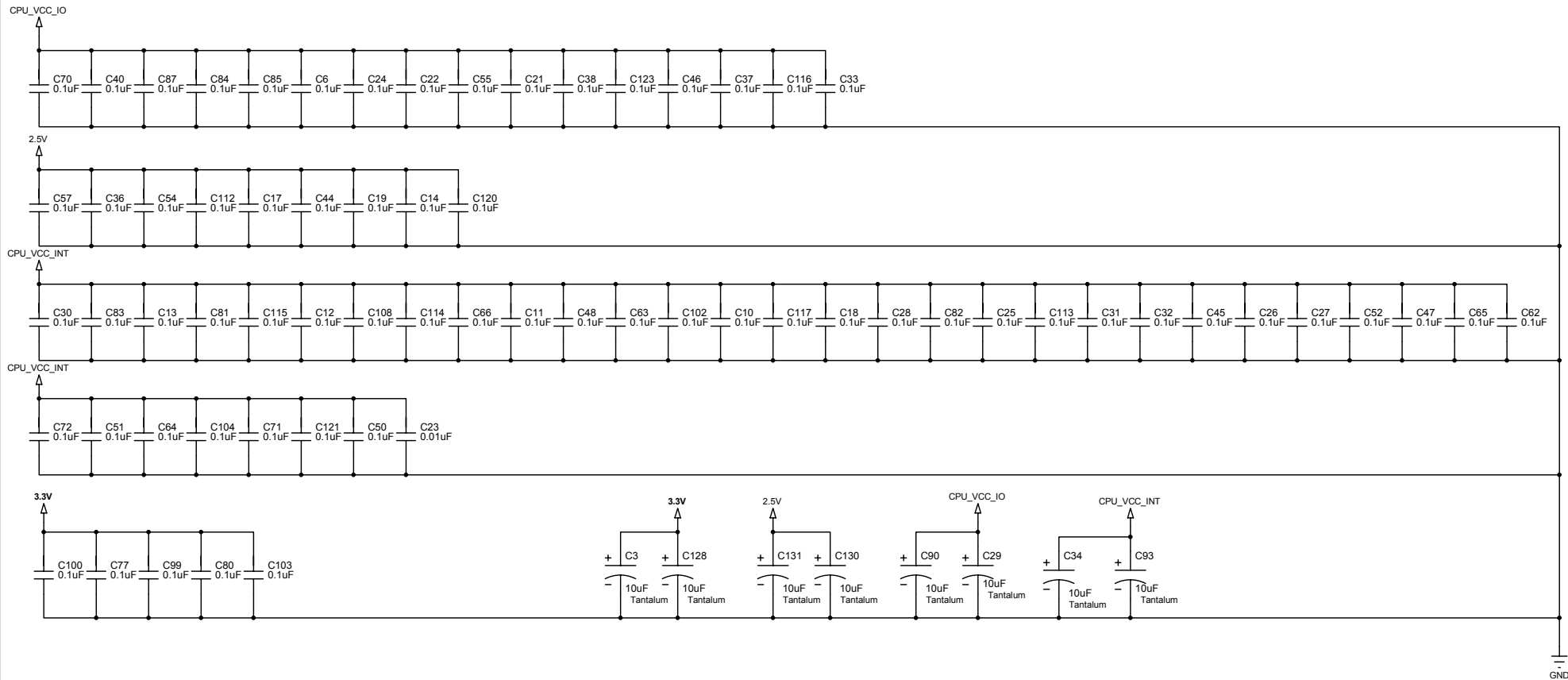
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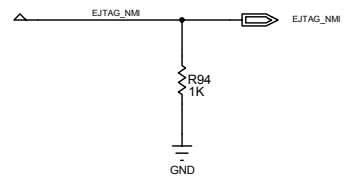
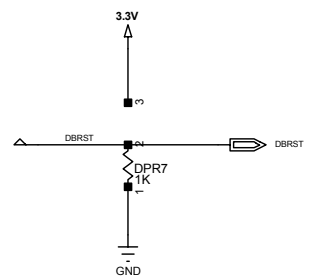
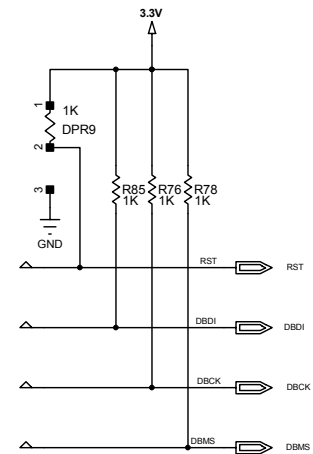
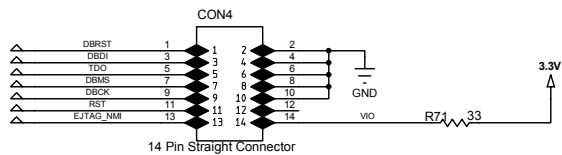
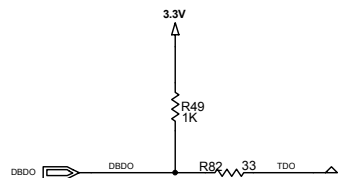
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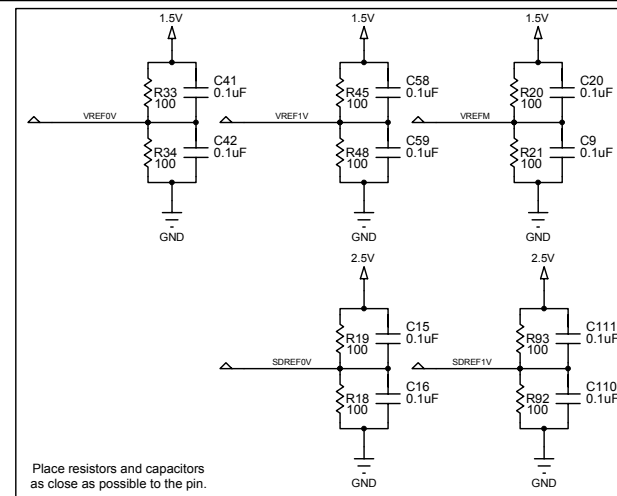
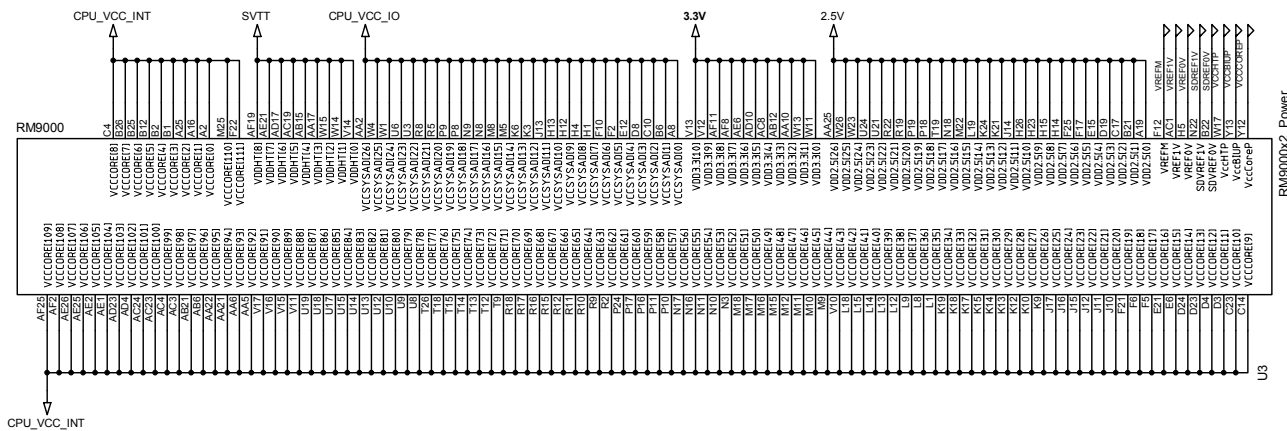
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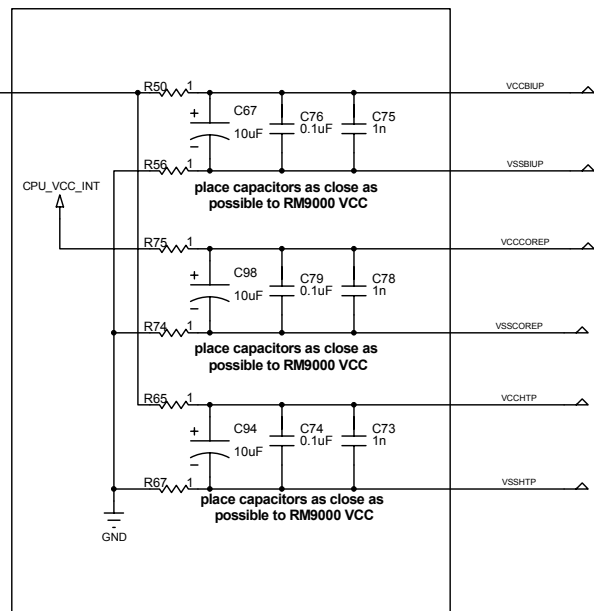
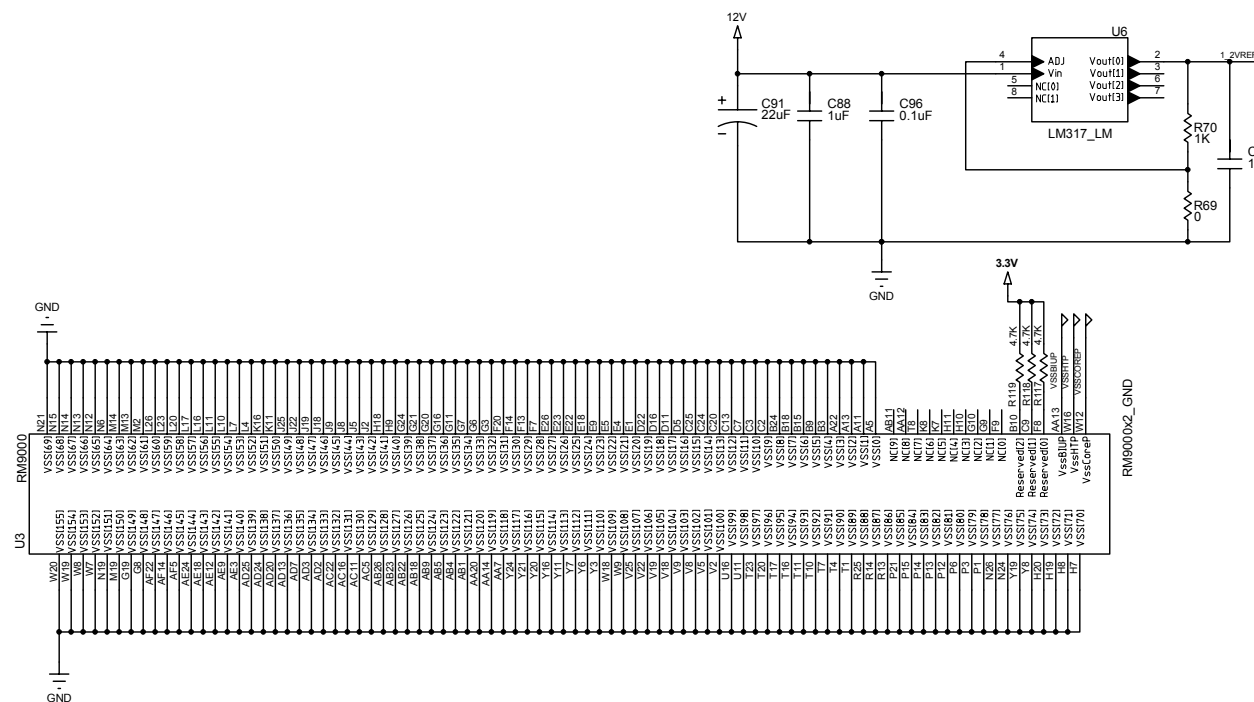




		File Name: EJTAG_CONNECTOR	REV
		Notes	2.0
SIZE B	Board Name: DB-RM9000X2-S7		SHEET @HierPageNumber CF @HierPageTotal



Place resistors and capacitors as close as possible to the pin.



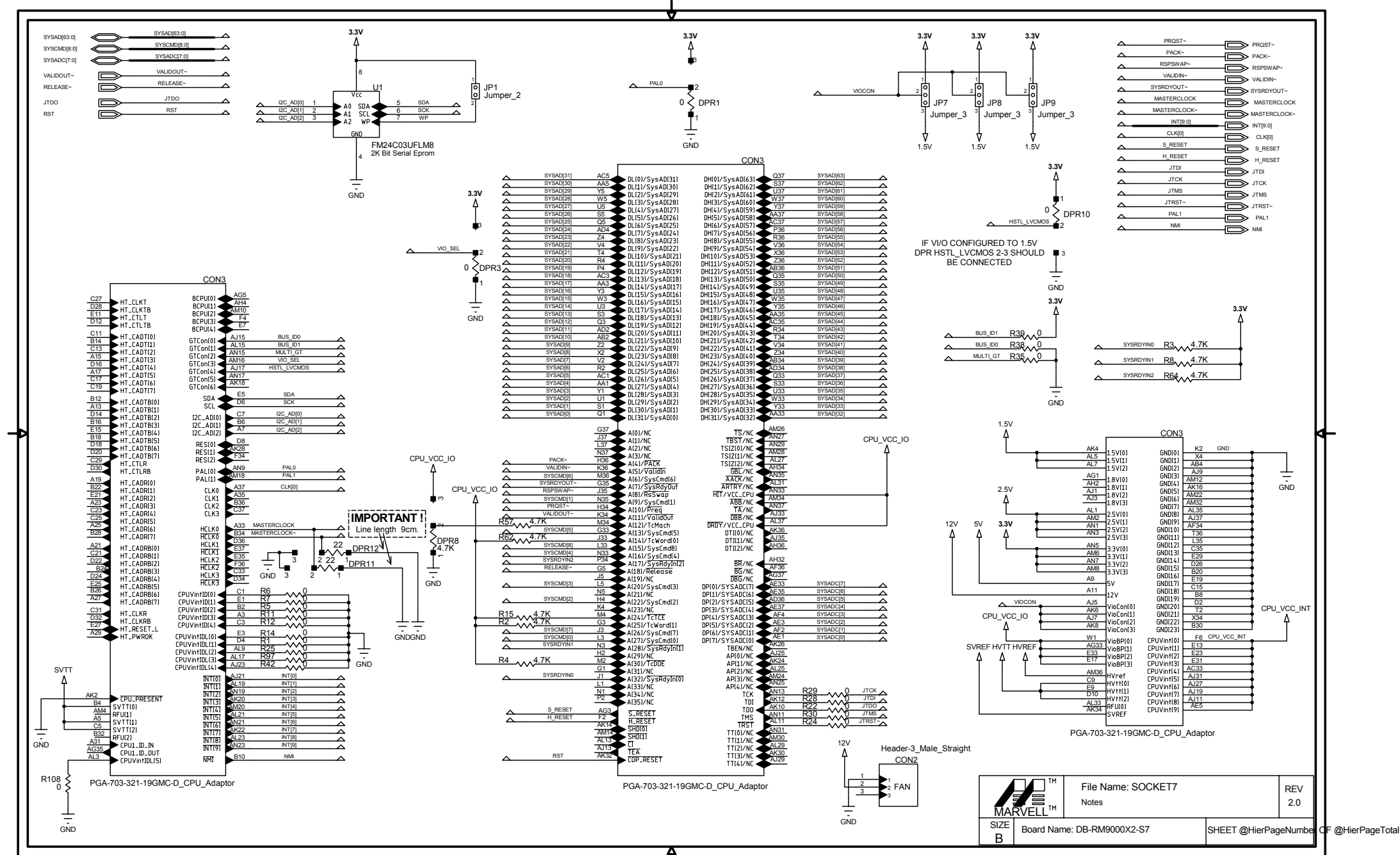
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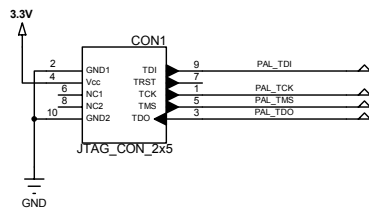
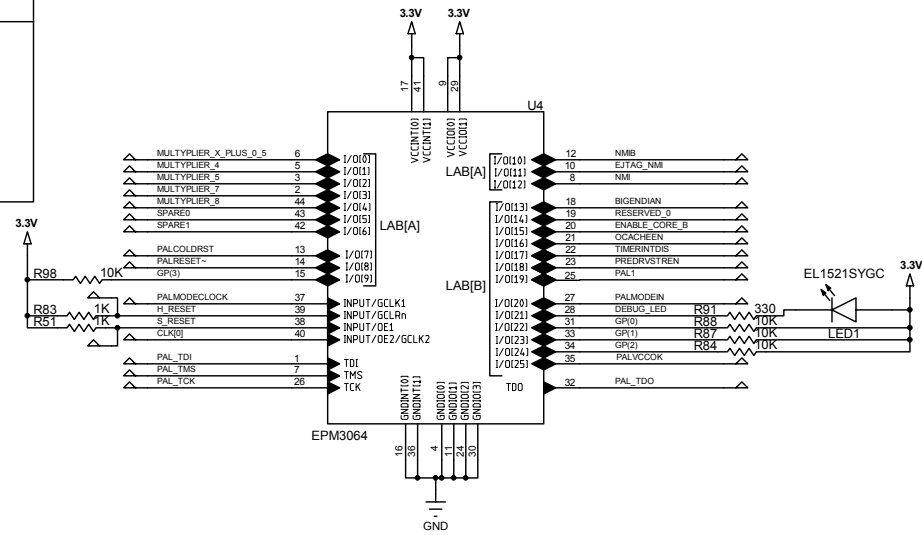
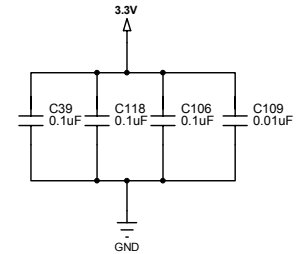
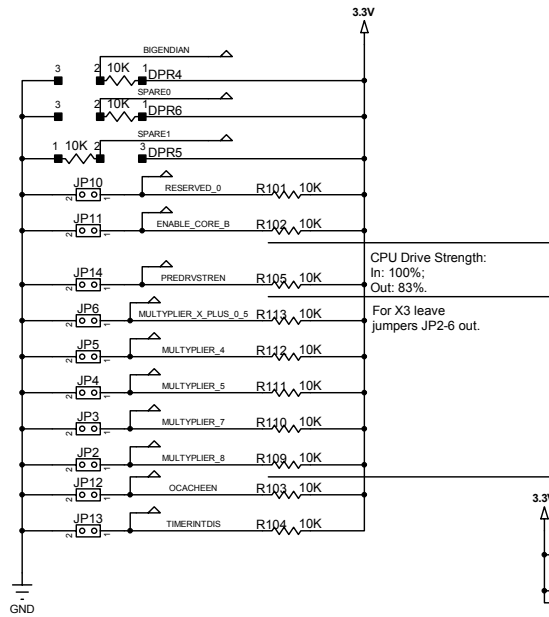
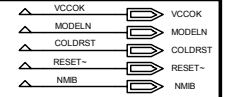
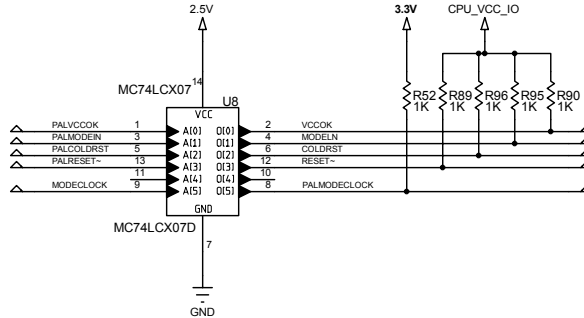
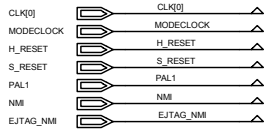
REV
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Board Name: DB-RM9000X2-S7

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 TSDDQS[7:0] TSDDQS[7:0]
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 TSDCS[0] TSDCS[0]
 SDCLK[3:0] SDCLK[3:0]
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 TSDCAS- TSDCAS-
 TSDRAS- TSDRAS-
 TSDWE- TSDWE-
 TSDBS[1:0] TSDBS[1:0]
 TSDCKE TSDCKE

