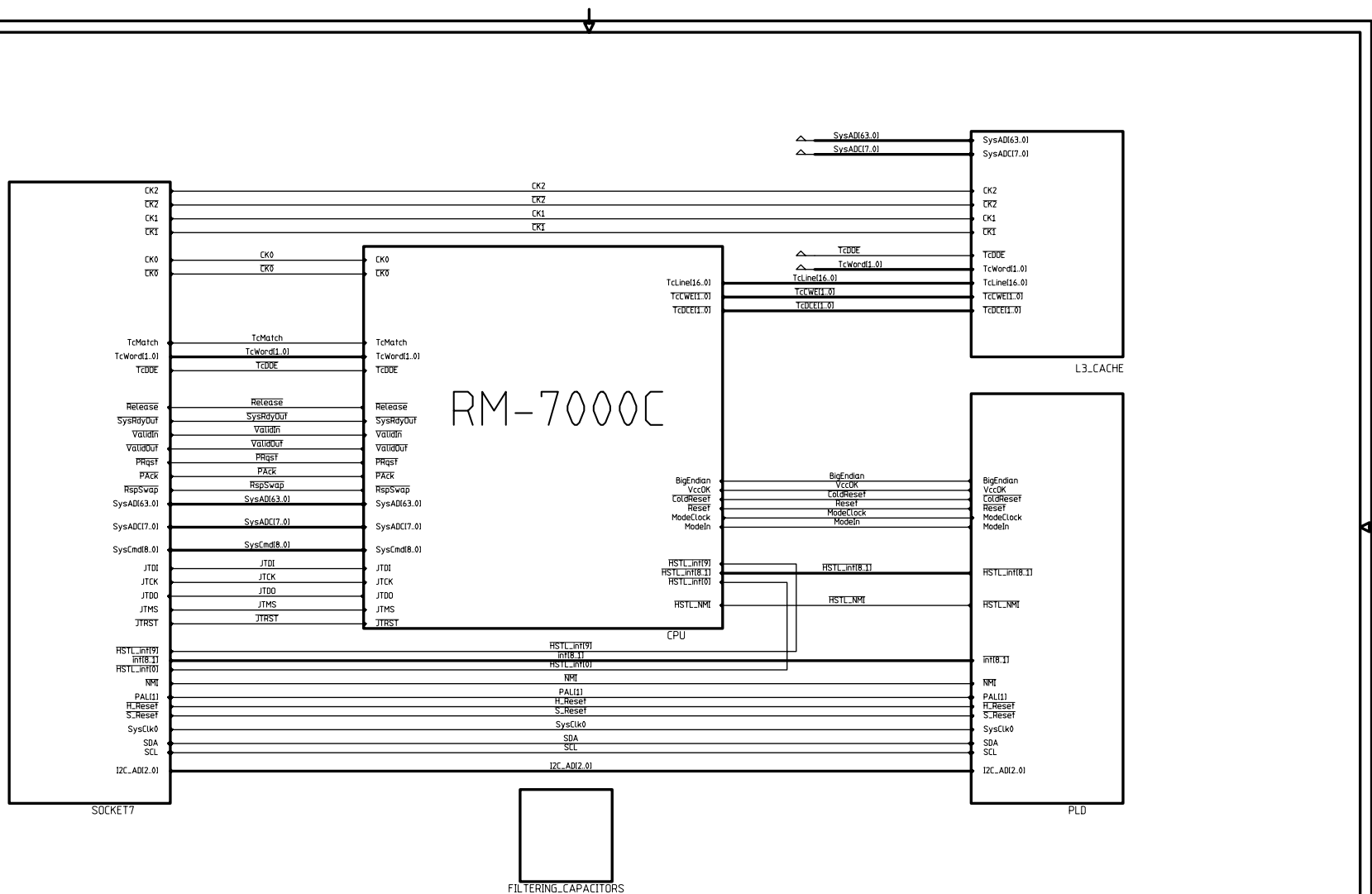
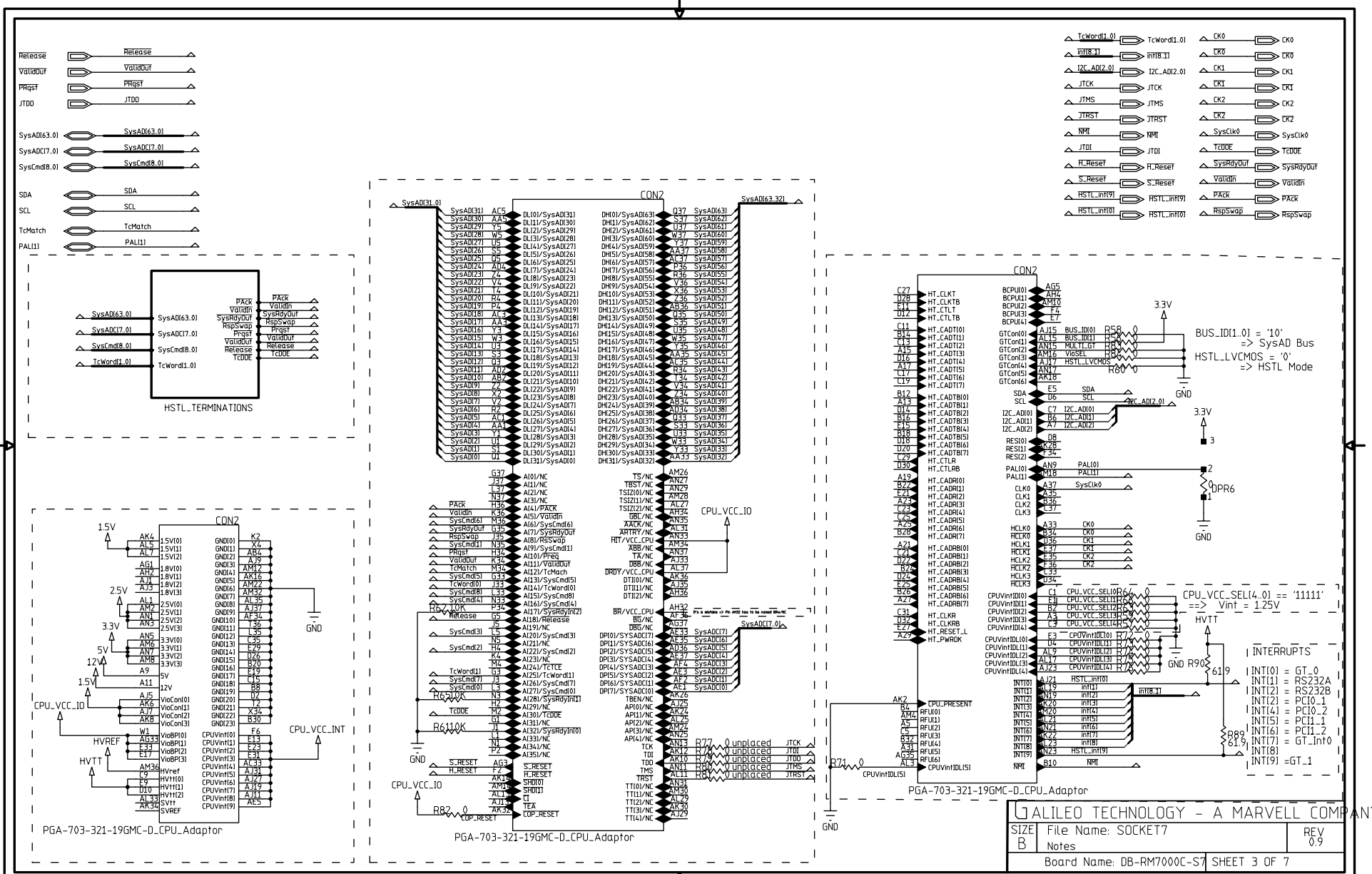


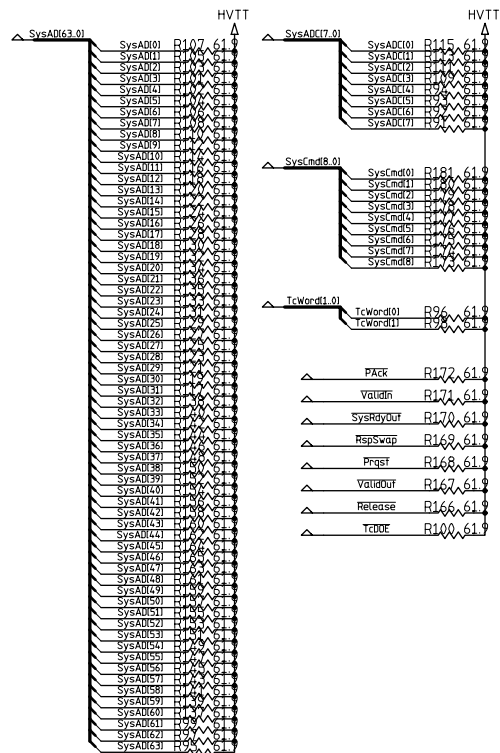


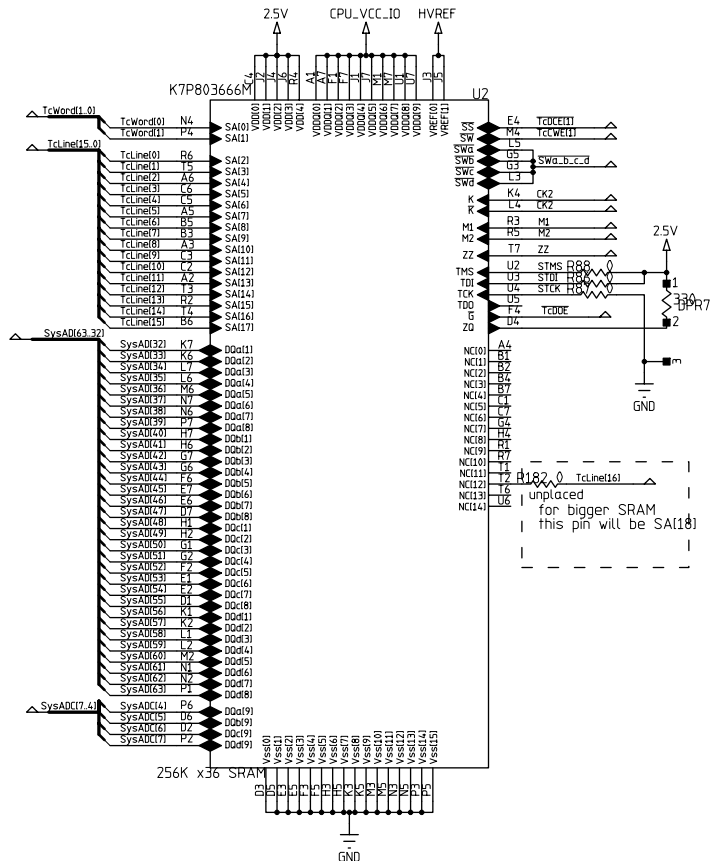
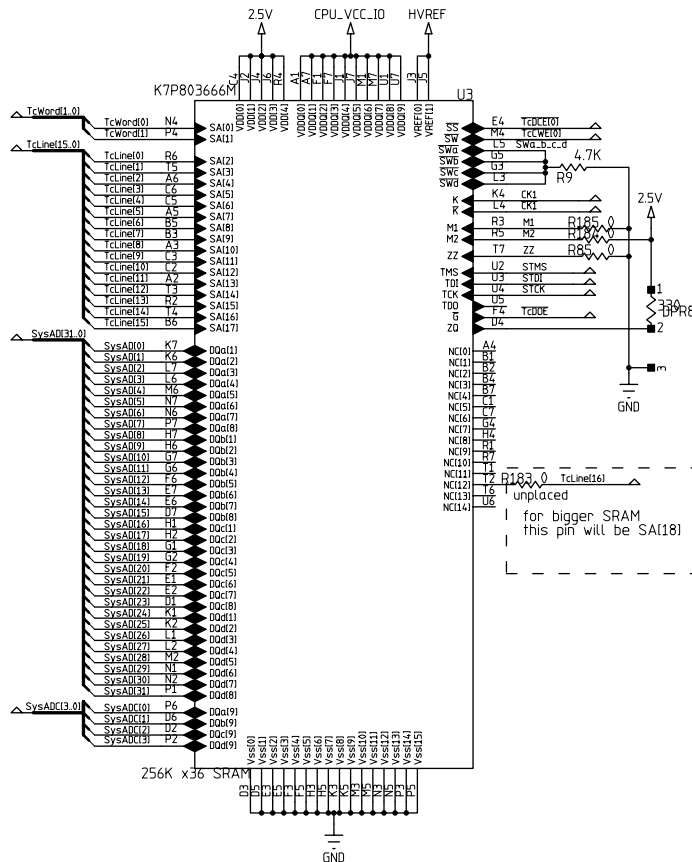
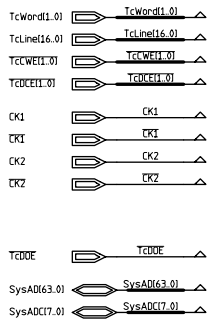


- | | | | |
|---------------|---------------|------------|------------|
| △ TcWord[1:0] | → TcWord1_01 | △ CK0 | → CK0 |
| △ int[8:1] | → int[8:1] | △ CK0 | → CK0 |
| △ I2C_AD[2:0] | → I2C_AD[2:0] | △ CK1 | → CK1 |
| △ JTK | → JTK | △ CK1 | → CK1 |
| △ JTMS | → JTMS | △ CK2 | → CK2 |
| △ JTRST | → JTRST | △ CK2 | → CK2 |
| △ NMI | → NMI | △ SysCk0 | → SysCk0 |
| △ JTDI | → JTDI | △ TCODE | → TCODE |
| △ H_Reset | → H_Reset | △ SysADout | → SysADout |
| △ S_Reset | → S_Reset | △ Validin | → Validin |
| △ HSTL_int[9] | → HSTL_int[9] | △ PACK | → PACK |
| △ HSTL_int[0] | → HSTL_int[0] | △ HspSwap | → HspSwap |

SysAD[63..0] ↔ SysAD[63..0] △
 SysADC[17..0] ↔ SysADC[17..0] △
 SysCmd[8..0] ↔ SysCmd[8..0] △
 TcWord[1..0] ↔ TcWord[1..0] △

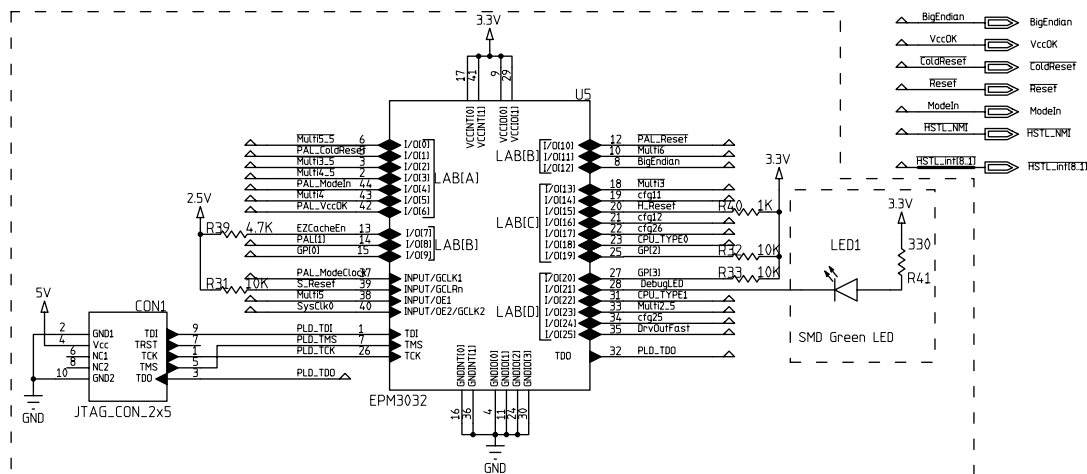
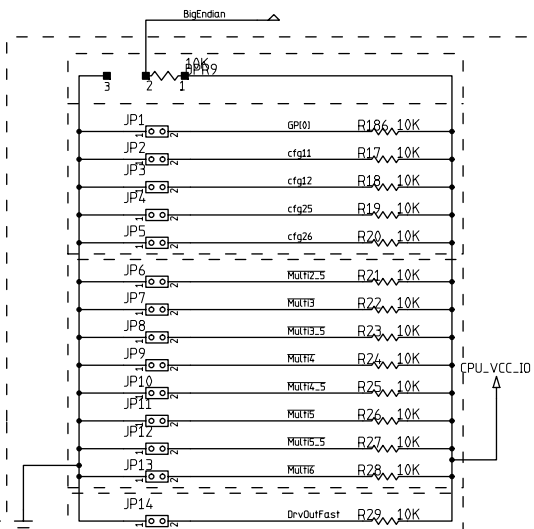
PACK ↔ PACK △
 ValidIn ↔ ValidIn △
 SysRdyOut ↔ SysRdyOut △
 RspSwap ↔ RspSwap △
 Prqst ↔ Prqst △
 ValidOut ↔ ValidOut △
 Release ↔ Release △
 TCODE ↔ TCODE △



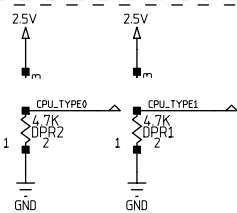


EZ Cache Mode

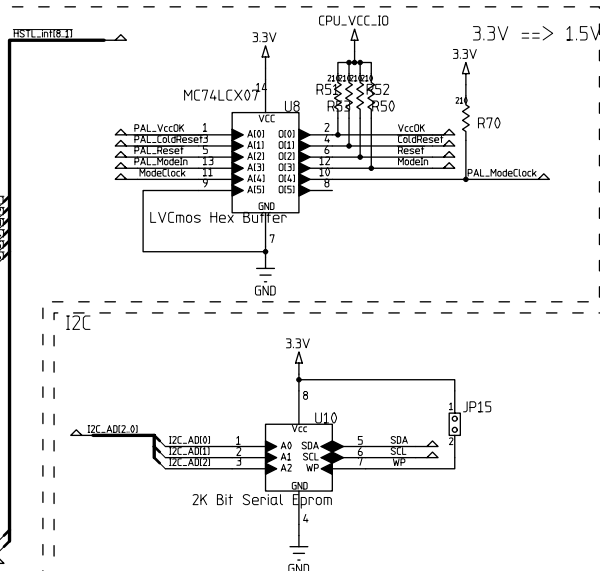
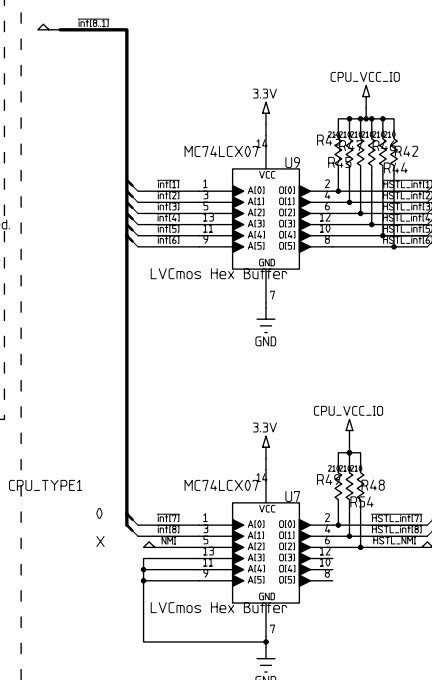
GALILEO TECHNOLOGY - A MARVELL COMPANY		
SIZE	File Name: L3-CACHE	REV
B	Notes	0.9
Board Name: DB-RM7000C-S7 SHEET 5 OF 7		



DPR	EndBit - Specifies byte ordering; logically OR-ed with the BIGEndian signal: 2-3 - Little Endian Ordering; 1-2 - Big Endian Ordering
J1	Reserved input to PAL
J2 (CF11)	TmrIntEn - Disables Timer Interrupt on int*(5); In - Timer Interrupt enabled, int*(5) disabled; Out - Timer Interrupt disabled, int*(5) enabled
J3 (CF12)	Tertiary Cache Interface: In - Disable Tertiary Cache; Out - Enable Tertiary Cache
J4 (CF25)	Enable Integrated Secondary Cache: In - Disable Cache; Out - Enable Cache
J5 (CF26)	Enable two outstanding reads with out-of-order return: In - Disable; Out - Enable
J6 - J13	CPU internal frequency Multiplier - the default value is x2. If more than one jumper will be inserted - the smallest value will be taken
J14	CPU output drive slew rate control: In - 100%; Out - 83%
J15	I2C Write Protected: In - write protected; Out - not protected.



CPU		CPU_TYPE0
RM7000A/B/C		0
RESERVED		X



SIZE B	File Name: PLD Notes	REV 0.9
Board Name: DB-RM7000C-S7 SHEET 2 OF 7		

