



Product Change Notification

GT-642xxA to GT-642xxB Revision Change

Summary

This Product Change Notification (PCN) describes the changes made to the Marvell® GT-642xx device between the GT-642xxA and the GT-642xxB revisions. The following devices are covered in this PCN.

Commercial	Industrial
GT-64240B-B-0-C1xx	GT-64240B-B-0-I1xx
GT-64241B-B-0-C1xx	GT-64241B-B-0-I1xx
GT-64242B-B-0-C1xx	GT-64242B-B-0-I1xx
GT-64260B-B-0-C1xx	GT-64260B-B-0-I1xx
GT-64261B-B-0-C1xx	GT-64261B-B-0-I1xx
GT-64262B-B-0-C1xx	GT-64262B-B-0-I1xx

Referenced Documents

This PCN references other documents from Marvell.

- Technical Bulletin TB-90: Differences Between the GT-64240A and the GT-64240B, MV-S100825-00 Rev. A
- Technical Bulletin TB-91: Differences Between the GT-64260A and the GT-64260B, MV-S100826-00 Rev. B
- Functional Errata and Restrictions, GT-64240A-B-0 and GT-64240B-B-0, MV-S500070-00 Rev. E
- Functional Errata and Restrictions, GT-64260A-B-0 and GT-64260B-B-0, MV-S500071-00 Rev. E
- Datasheet, GT-64240A/B, MV-S100413-00, Rev. B
- Datasheet, GT-64260A/B, MV-S100414-00, Rev. B

Product Changes

The table below summarizes all changes between revisions.

Item	Change	
	Old	New
Device Number and Order Code	GT-6424xA-B-0-C1xx GT-6426xA-B-0-C1xx	GT-6424xB-B-0-C1xx GT-6426xB-B-0-C1xx GT-6424xB-B-0-I1xx GT-6426xB-B-0-I1xx

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Item	Change	
	Old	New
Device Foundry	TSMC	No change
Fabrication process	0.18u 6 layer metal	No change
Cell library	Avant!	No change
Package(s)	665-pin PBGA	No change
Assembly contractor	No changes	
Functional changes	Refer to the revision history section in the related document datasheets.	
Errata changes	Summarized in the referenced errata documents	
Specification changes	See the following Changes section	
Reason(s) for Revision	Errata corrections	

Changes Highlights



Note

Except for the errata fixes, all changes are catalogued in the respective datasheet revision history section.

AC Timing Parameters

New AC parameters for industrial 100 MHz device.

Certain timing parameters have changed. See the AC timing section in the referenced datasheet.

Errata

See the referenced functional errata and restriction documents for details about applicable errata.

PCI Deadlock and Ordering Modes

The GT-642xxB introduces new PCI deadlock avoidance and ordering features, see the referenced datasheets for details.

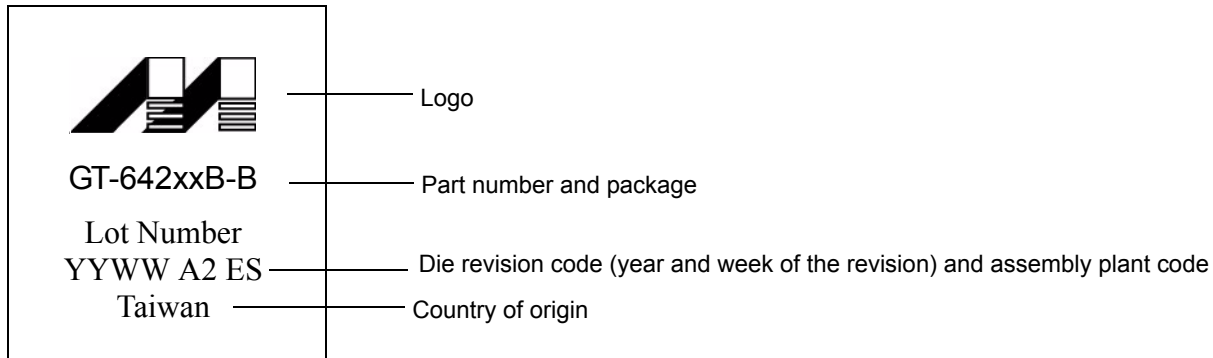
Conversion Timeframe

Marvell is currently in production with the GT-642xxA devices. The GT-642xxB is available in production now.

Current GT-642xxA customers should convert to the GT-642xxB versions by August 1, 2003. Marvell will accept orders for the GT-642xxA until May 1, 2003. All future orders after May 1, 2003 must be for the GT-642xxB devices.

Contact Marvell to receive samples/qualification units for rev B devices, which are available now. Orders for rev B devices can be placed now. All production orders have a lead time of 12 weeks.

Part Markings



Preliminary Information

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