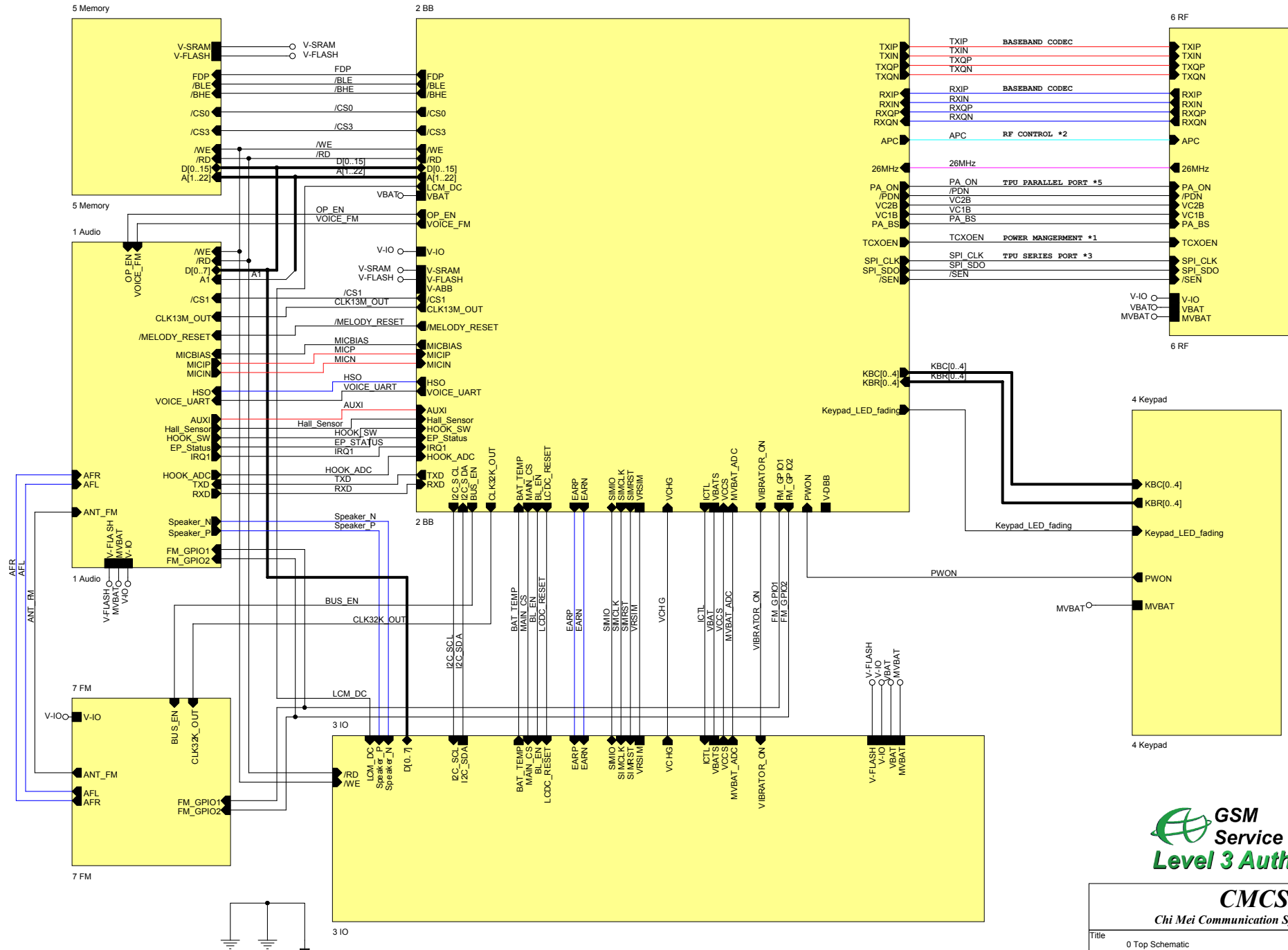
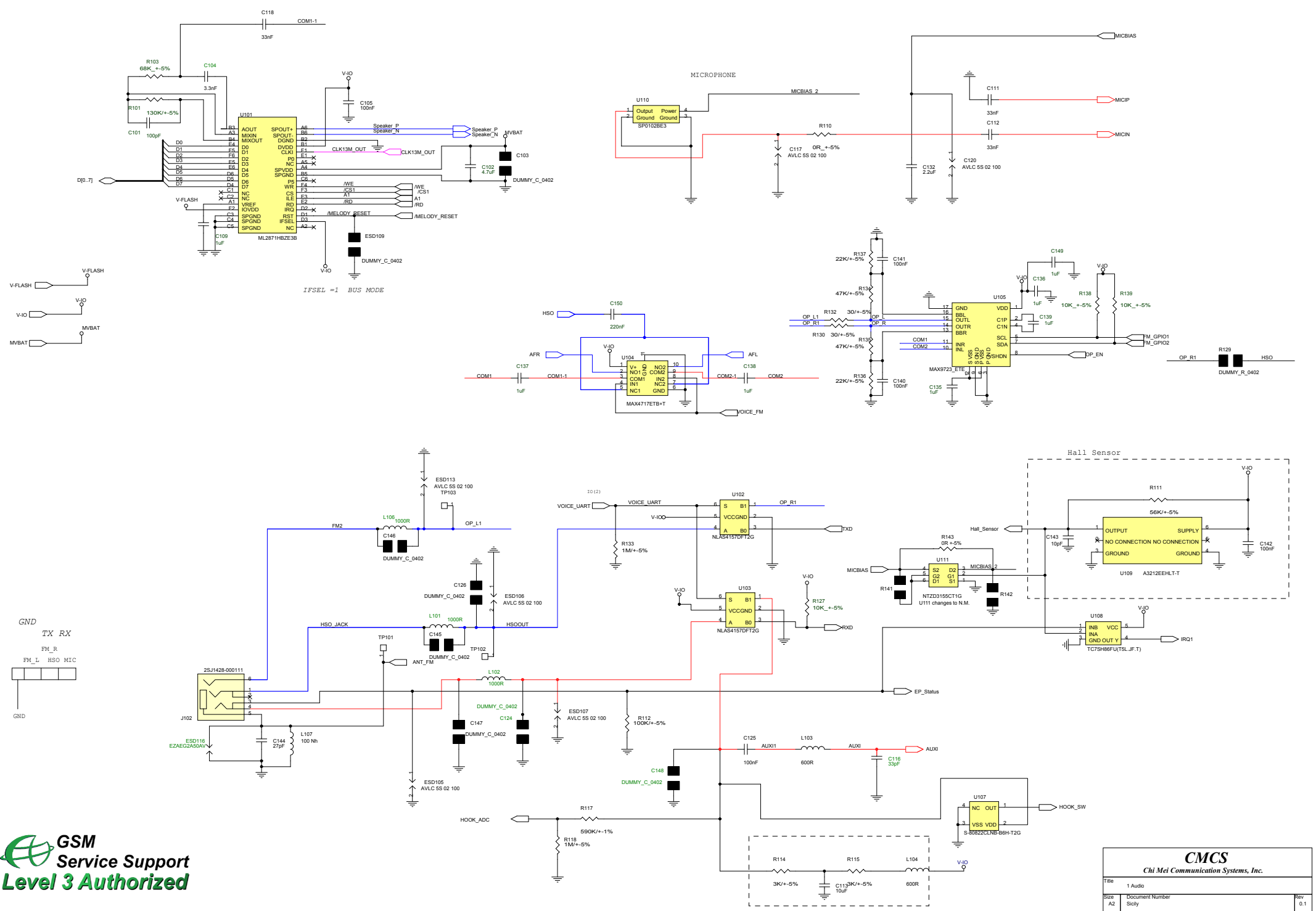


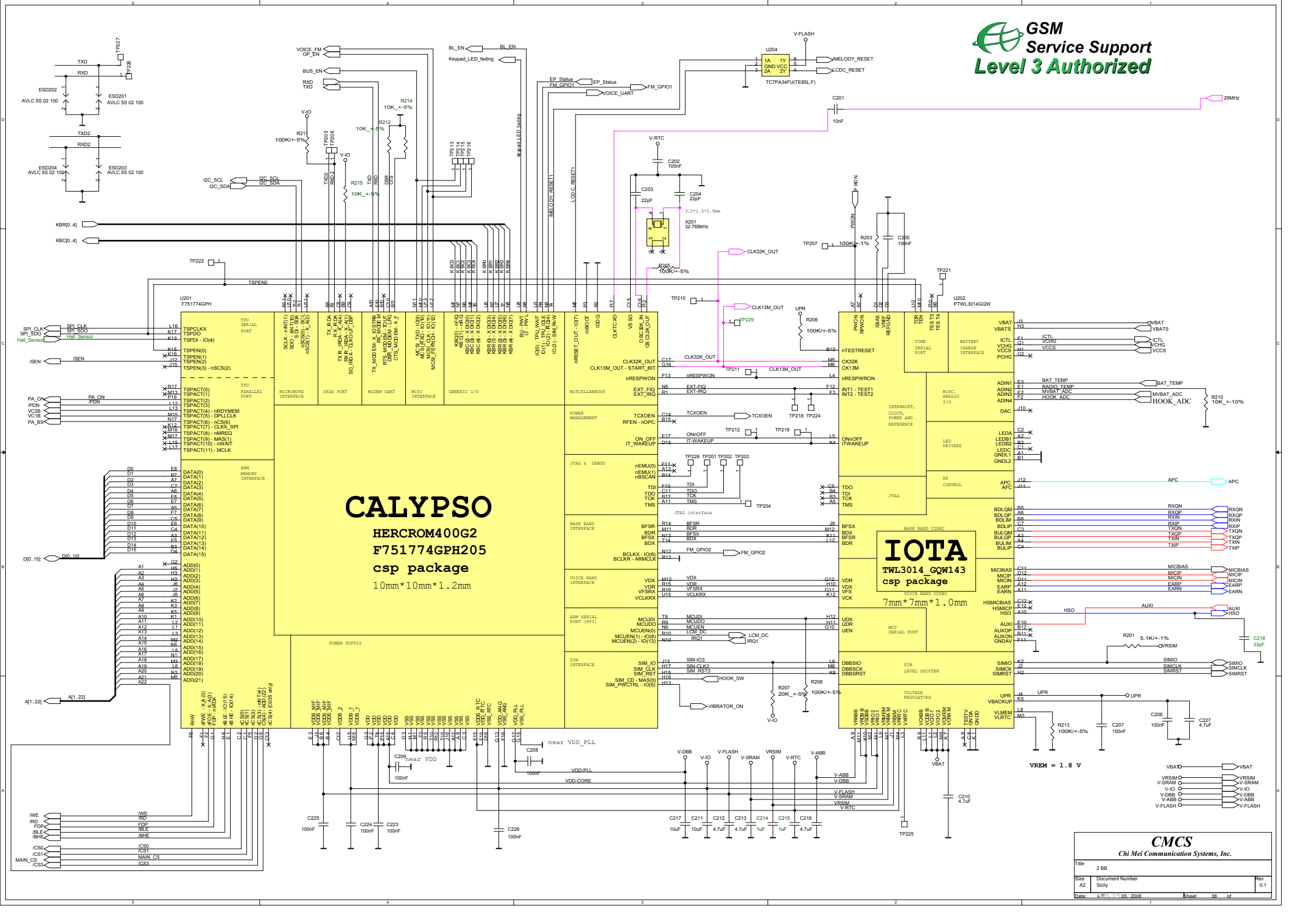
# Lower Board



**GSM**  
Service Support  
**Level 3 Authorized**

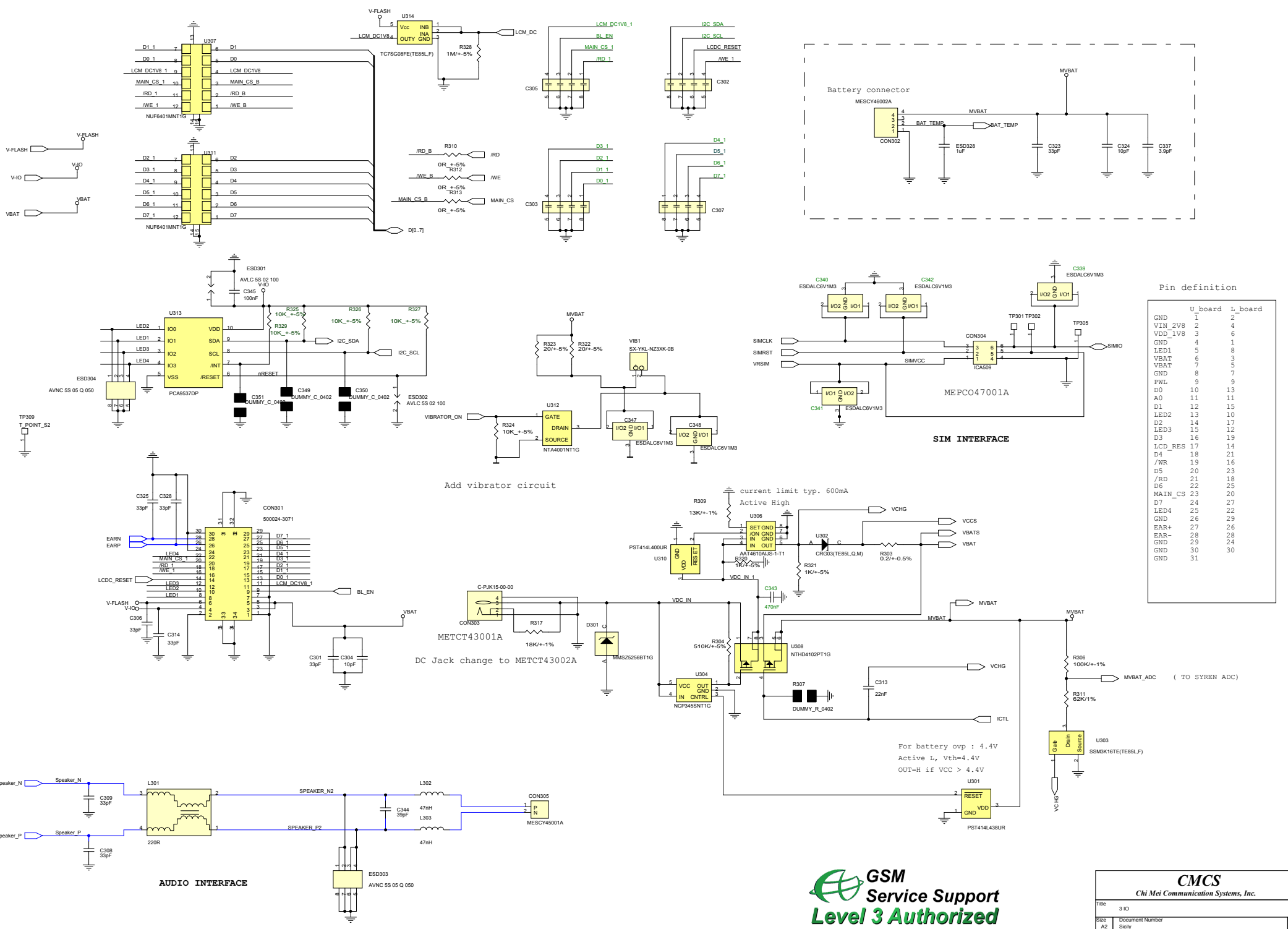
<b>CMCS</b> Chi Mei Communication Systems, Inc.		
Title	0 Top Schematic	
Size	Document Number	Rev
A3	Sicily	0.1
Date	日期: 2005.05.2006	Sheet 18 of





**CALYPSO**  
**HERCROM400G2**  
**F751774GPH205**  
csp package  
10mm\*10mm\*1.2mm

**IOTA**  
**TWL3014\_GQW143**  
csp package  
7mm\*7mm\*1.0mm



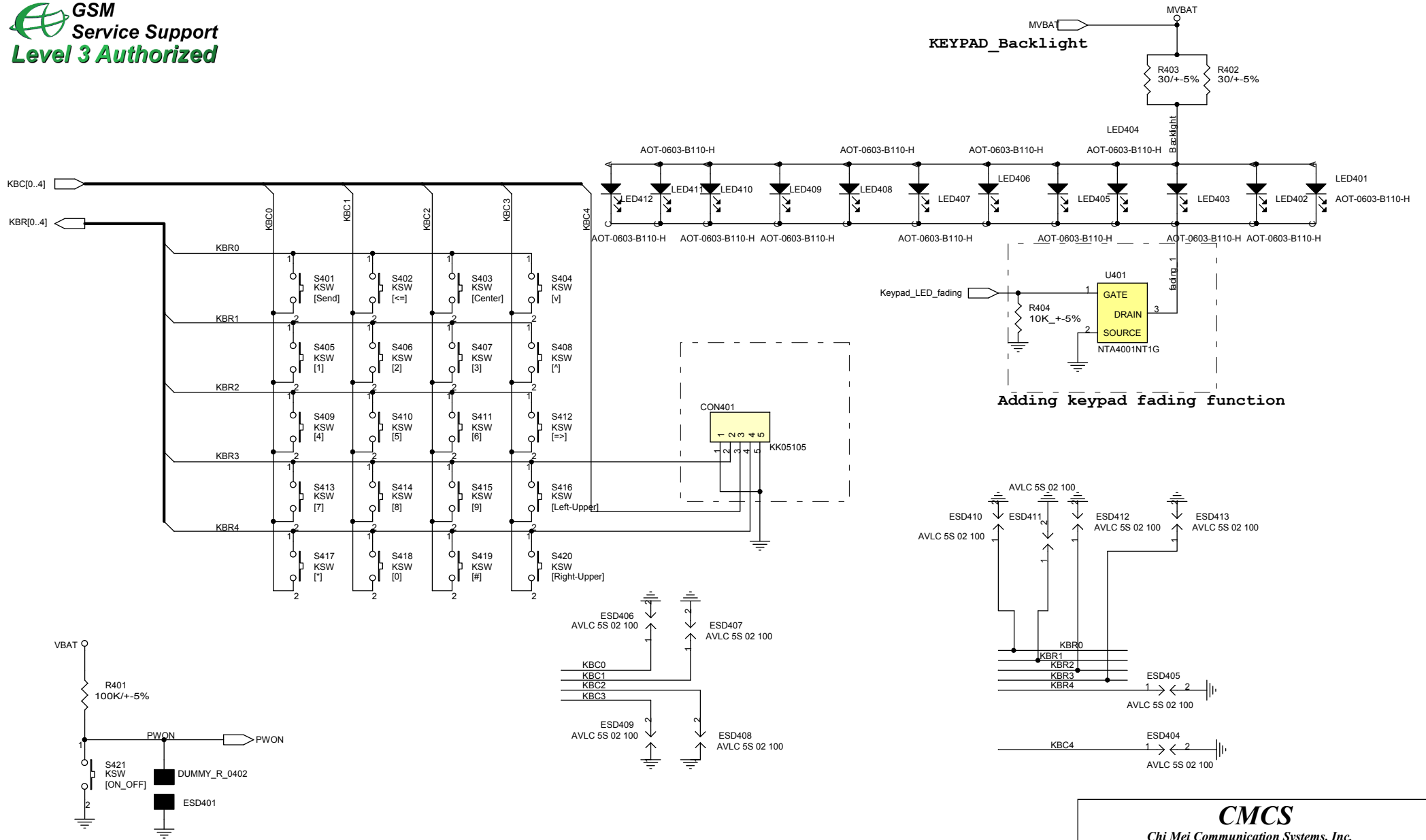
Pin definition

	U_board	L_board
GND	1	2
VIN_2V8	2	4
VDD_1V8	3	6
GND	4	1
LED1	5	8
VBAT	6	3
VBAT	7	5
GND	8	7
FWL	9	9
D0	10	13
A0	11	11
D1	12	15
LED2	13	10
D2	14	17
LED3	15	12
D3	16	19
LCD_RES	17	14
D4	18	21
/WR	19	16
D5	20	23
/RD	21	18
D6	22	25
MAIN_CS	23	20
D7	24	27
LED4	25	22
GND	26	29
EAR+	27	26
EAR-	28	28
GND	29	24
GND	30	30
GND	31	30

For battery ovp : 4.4V  
 Active L, Vth=4.4V  
 OUT=H if VCC > 4.4V

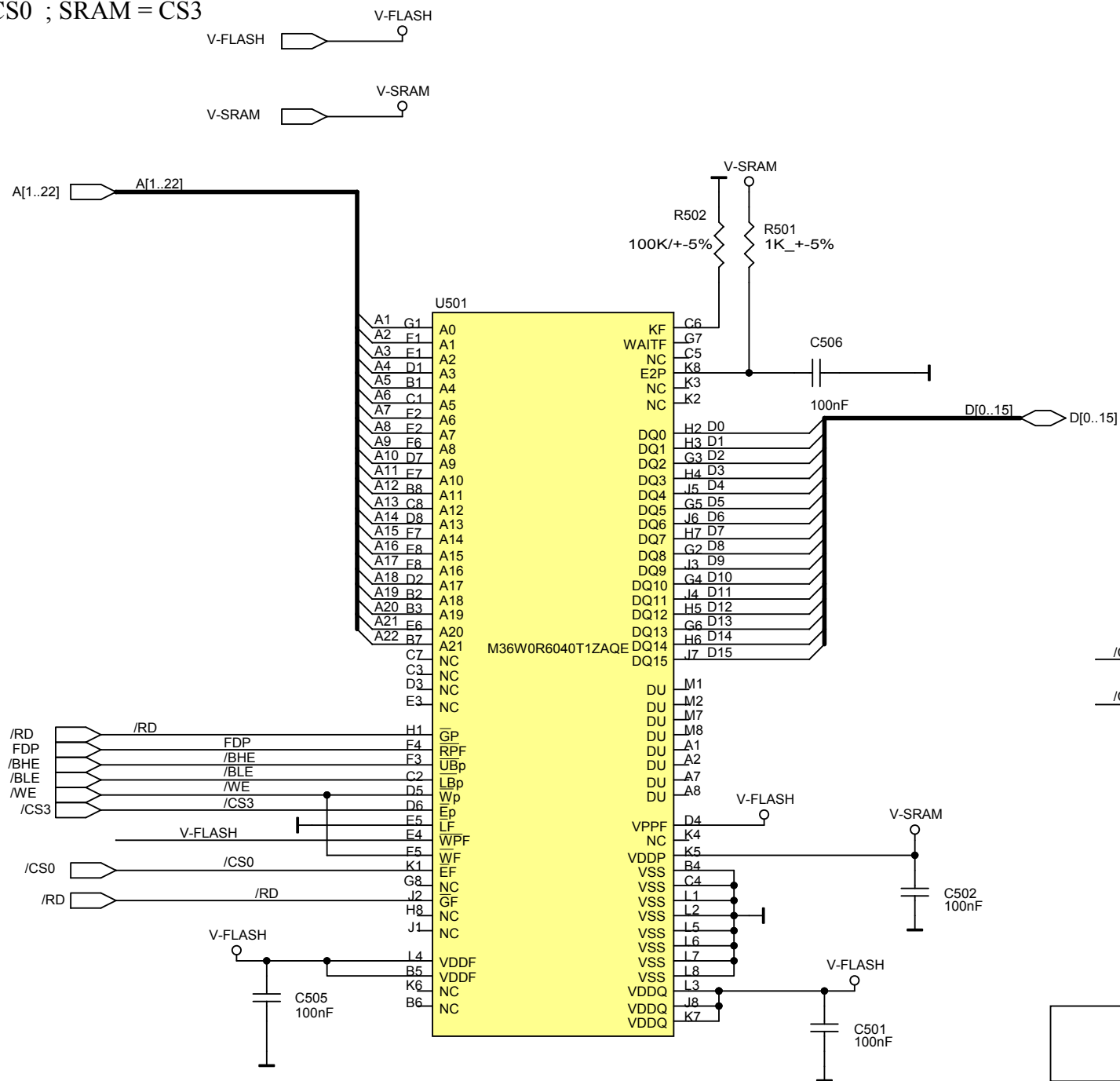
<b>CMCS</b> Chi Mei Communication Systems, Inc.	
Title	310
Size	Document Number
A2	Sicily
Date:	07/06/2008
Sheet	48 of

**GSM**  
 Service Support  
 Level 3 Authorized



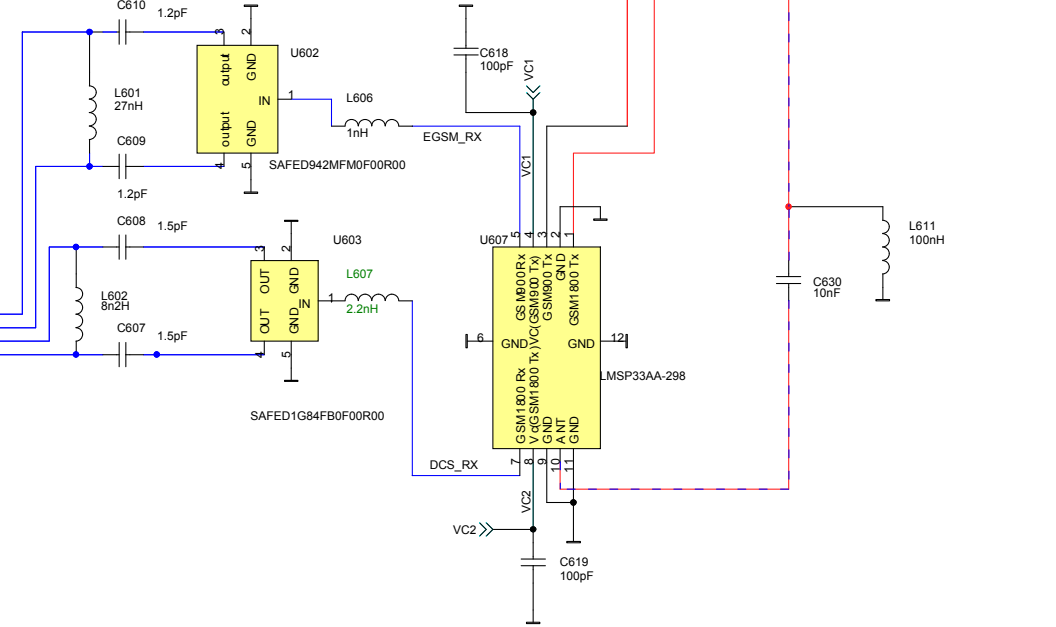
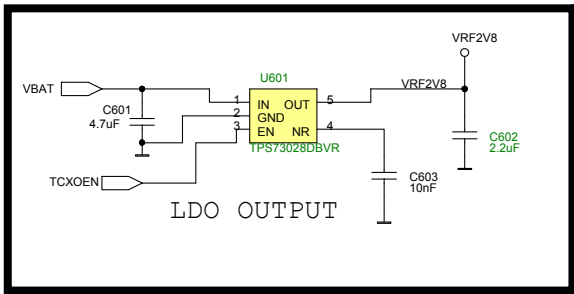
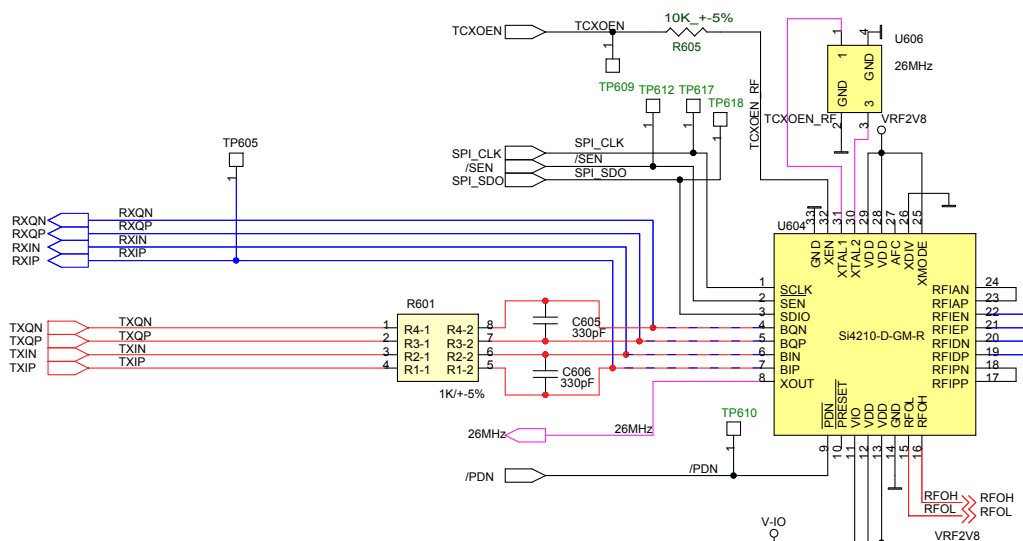
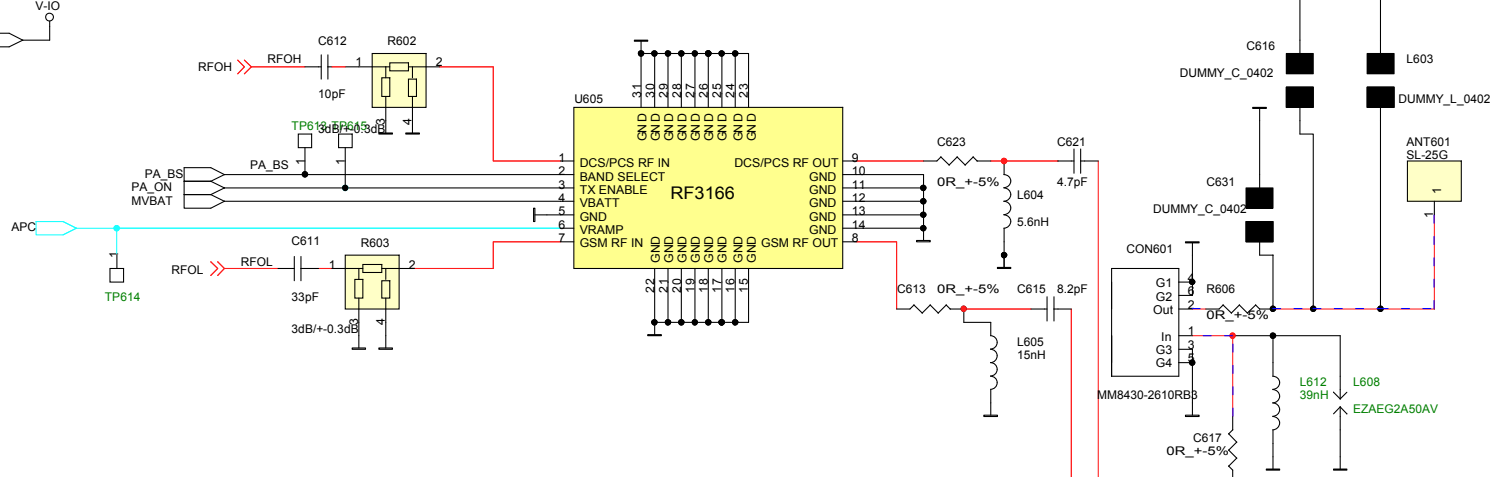
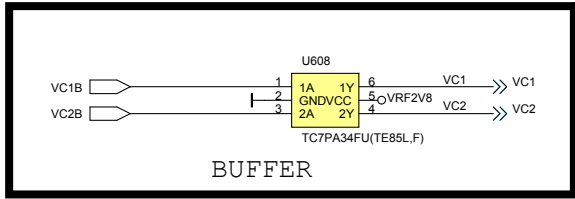
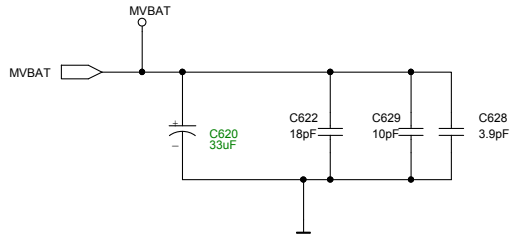
<b>CMCS</b> Chi Mei Communication Systems, Inc.		
Title 4 Keypad		
Size B	Document Number Stilly	Rev 0.1
Date 2006.05.05	Sheet 58	of 1

Flash = CS0 ; SRAM = CS3

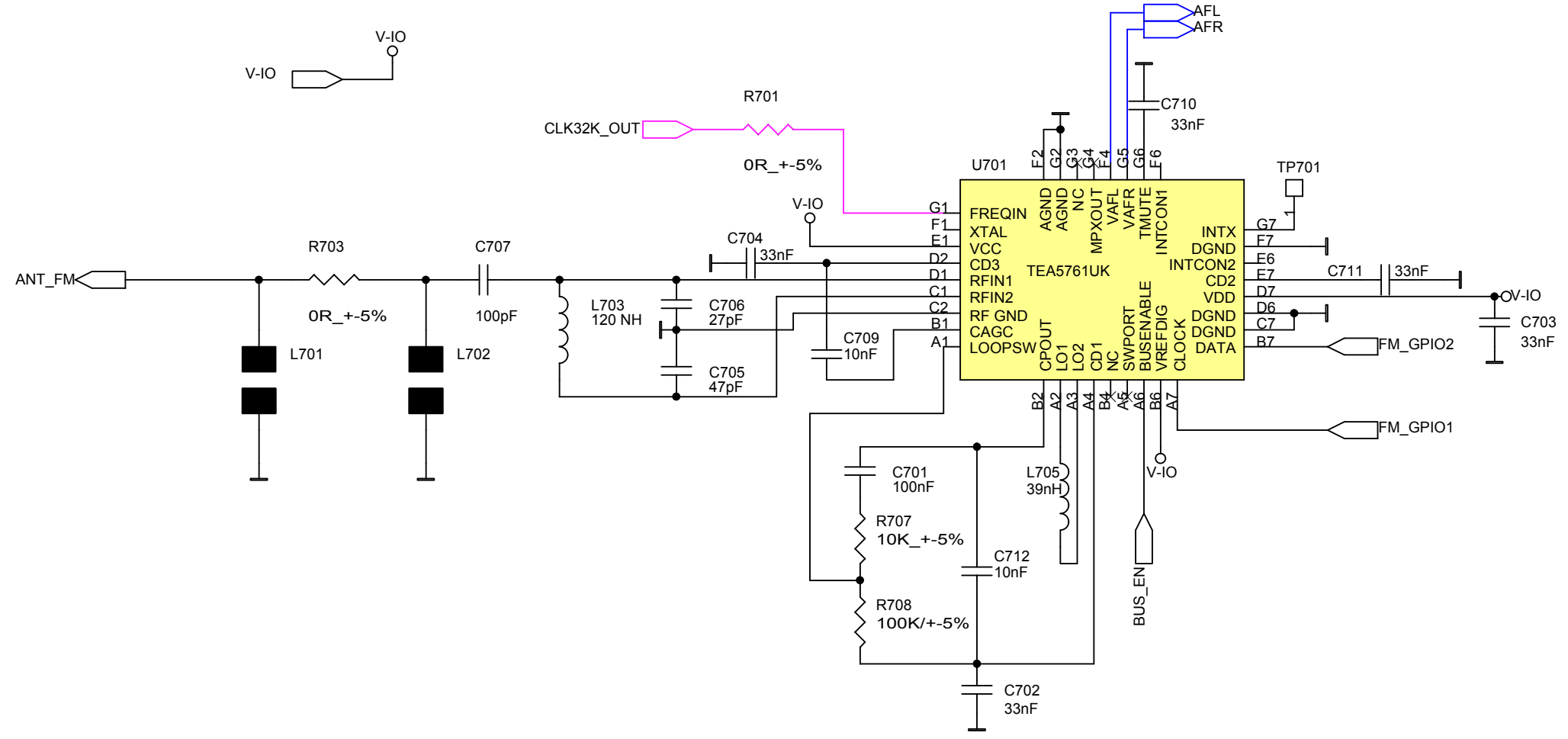


**64Mb NOR Flash + 16 Mbit PSRAM**  
VIO = VCC = 1.8V

<b>CMCS</b> Chi Mei Communication Systems, Inc.		
Title 5 Memory		
Size A4	Document Number Sicity	Rev 0.1
Date: 星期日, 2005 2006	Sheet 68	of 1



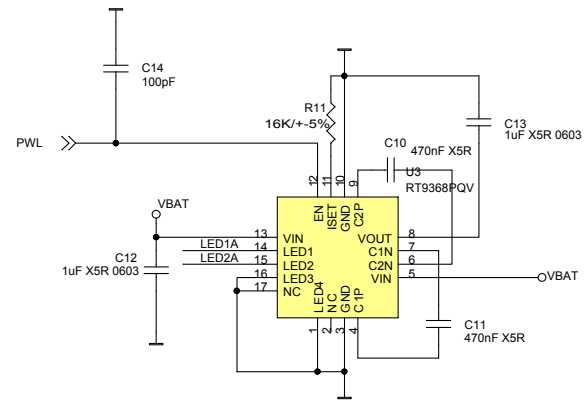
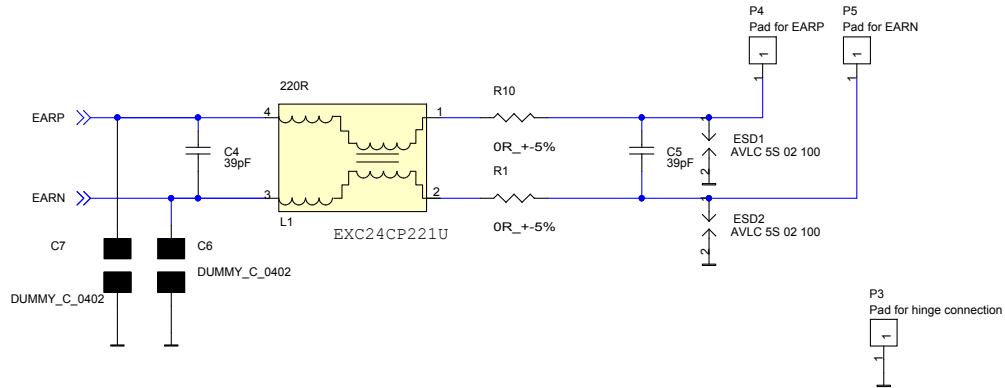
Title	6 RF	
Size	Document Number	Rev 0.1
Date:	星期五, 2006年05月05日	Sheet 78 of



Title		
7 FM		
Size	Document Number	Rev
A	Sicily	0.1
Date:	星期二, 2006年05月02日	Sheet 88 of



# Upper Board



**Richtek WHITE LED BACKLIGHT DRIVER IC**

**Unused LEDx should be connected to ground.  
Connect center NC pad to ground.**

